

Claims

1-8 Canceled

9. (New) A method for operating a transmitting device of an access system with a plurality of long wave antennas, the method comprising:
 - jointly triggering the long wave antennas by a central power amplifier;
 - individually activating the long wave antennas by a multiplexer device; and
 - regulating a transmitter current.
10. (New) A method according to claim 9 further comprising:
 - detecting an actual value of the transmitter current is detected and if a desired value is exceeded, the transmitter current is approximated to the desired value by pulse-width modulation of an input signal of the central power amplifier.
11. (New) A method according to claim 9, wherein the power amplifier is utilized to generate (2) a square wave or trapezoidal output voltage (LF_{out}) for triggering the long wave antennas.
12. (New) A transmitting device for an access system, comprising:
 - a plurality of long wave antennas;
 - a multiplexer device for activating at least one long wave antenna;
 - a joint amplifier device having an output, wherein the long wave antennas are jointly connected; and
 - a control unit (10) for regulating a transmitter current.

13. (New) A transmitting device according to claim 12 further comprising:

a device for detecting an actual value of the transmitter current; and

a control unit for pulse-width modulation of an input signal of the amplifier device, wherein the control unit initiates the transmitter current to approximate a desired value, if the desired value is exceeded.

14. (New) A transmitting device according to claim 13 further comprising:

the control unit (10) is utilized to limit the transmitter current connected upstream to the joint amplifier device on the input side and downstream to the multiplexer unit.

15. (New) A transmitting device according to one of claims 14 further comprising:

a control device (14), connected on the output side to a control input of the joint amplifier device, wherein the control device comprises a first input for a clock signal and second input for a control signal.

16. (New) A transmitting device according to claim 15, in which the control device (14) comprises a logical combination element (16) with a first input for the clock signal and with a second input connected to a comparator (12) is connected on the output side via a sequential circuit (18), wherein the sequential circuit (18) is provided as a controlled latch-flipflop for pulse-width modulation of a control input signal (P_{in}) of the joint amplifier device.